



**VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, ODISHA**  
**P.O: Engineering College Burla (Siddhi Vihar), Dist: Sambalpur**  
**Odisha– 768018, India**

No. VSSUT/EEE/ 856 / 2022,

Date: 03.11.2022

**Advertisement for Post of Research Assistant (One position) for State Council on  
Science & Technology, Govt. Of Odisha, Sponsored Project**

Applications are invited from eligible students for the position of Research Assistant, a Project fellow (One-Position) for the State Council on Science & Technology, Odisha, Govt. of Odisha funded project, under the principal investigator Dr. Gyan Ranjan Biswal, Associate Professor, Department of Electrical & Electronics Engineering, Veer Surendra Sai University of Technology, Burla, Sambalpur, Odisha. Interested and eligible candidates are requested to send Hard copy of completed application (Annexure -1) along with self-attested copies of supporting documents to the Registrar, Veer Surendra Sai University of Technology, Burla, Sambalpur-768018, on or before Nov.20, 2022, by speed/registered post/courier. The details of the project are as follows:

<b>Name of the Project</b>	Smart Sensors Enabled Optical Wireless Data Acquisition for Smart-Grid Applications
<b>Principal Investigator</b>	<b>Dr. Gyan Ranjan Biswal</b>
<b>Qualification</b>	<b>Essential:</b> M.E. or M.Tech. in Instrumentation and Control Engineering/ Power System Engineering/ Communication Engineering/ Sensors and IoT/ Cyber Physical System /Instrumentation and Signal Processing or relevant specialization from a recognized institute/university with minimum 60% marks or equivalent CGPA. <b>Desirable:</b> Research Experience of 2 years and/or GATE Qualified
<b>Fellowship</b>	As per revised DST (Govt. of Odisha) Rules. For <b>Project fellow (Research Assistant: ₹17, 000 per month</b>
<b>Nature and duration of post</b>	The work will involve design of suitable sensor for real time GIS Analysis along with establishing optical wireless communication and power link in between the developed modules. The appointment will be purely temporary basis and will be co-terminus with the project. The PI and Institute reserve the right to terminate the engagement of the PA at any time, if the performance is not found satisfactory. The person engaged shall not be entitled for any claim implicit or explicit for permanent absorption in the University.

### **General Term and Condition:**

- Interested and eligible candidates are requested to send Hard copy of completed application (Annexure -1) along with self-attested copies of supporting documents to **The Registrar, Veer Surendra Sai University of Technology, Burla, Sambalpur-768018, on or before 20 November 2022 by Speed/Registered Post/Courier.** University will not be responsible for any postal delay of application. Applications received after the above-mentioned date will be treated as cancelled. On the envelope, please inscribe **“Application for the post of Research Assistant (DST, Govt. of Odisha), Department of Electrical and Electronics Engineering (EEE), VSSUT, Burla-768018, Sambalpur (Odisha)”**.
- Candidates will be shortlisted for the interview based on the specialization and availability of candidates and other requirements as per the project.
- The date and time of interview will be informed via email or phone and only short-listed candidates will be allowed to attend the interview.
- Candidates shortlisted for interview should appear in person with originals of certificates (one set of Self-Attested Photocopies), and Date of Birth Proof along with other relevant supporting documents (like copies of publications, awards etc.)
- No travelling or any other allowances is admissible for attending the interview.
- Candidates will not be considered in absentia.
- The Principal Investigator of the project reserves the right to fill or not fill position or withdraw the notification at any point of time and in any conflict the decision of PI and Institute will be final.
- All legal implications are subjected to local jurisdiction only.



**(Dr. Gyan Ranjan Biswal)**  
**Principal Investigator**

## ANNEXURE-1

Application form for the Post of Project Fellow (PF) for DST, Govt. of Odisha, sponsored Research Project

### Personal Details:

1. Full Name (In capital):
2. Date of Birth (DD/MM/YY) :
3. Category (GEN/SC/ST/OBC)
4. Marital Status (Single/Married)
5. Gender (Male/Female)
6. Nationality
7. Address for Communication
8. Permanent Address
9. Mobile/Phone No.
10. Email:

Affix a  
recent  
passport  
size photo

### Details of School/University/Institute Studied (From Matriculation Onward)

Degree	Discipline	University/Institute	Year	%Marks /CGPA	Division
Class X					
Higher Secondary					
B.Tech.					
M.Tech.					
Any other					

Qualifying Examination (GATE/CSIR-NET/ICAR-NET/UGC-NET) if any:

Professional Experiences (Teaching/Research/Industrial) if any:

Name of Organization	Designation	Nature of Work	From	To

ME/MTech Thesis Title:

Seminar/workshop/conference attended (use extra sheet if needed):

Name of Seminar/Conference, etc.	Paper presented	Organized by

Research Publication including book and book chapters (use extra sheet if needed)

Awards, patents, prizes etc. (use extra sheet if needed):

Any other Relevant Information (use extra sheet if needed):

**DECLARATION:**

I hereby declare that I have carefully read the instructions and particulars supplied to me and that the entries made in this application form are correct to the best of my knowledge and belief. If selected for admission, I promise to abide by the rules and discipline of the Institute. I note that the decision of the Institute is final in regard to selection. The Institute shall have the right to expel me from the Institute at any time after my admission, provided it is satisfied that I was admitted on false particulars furnished by me or my antecedents prove that my continuance in the Institute is not desirable. I agree that I shall abide by the decision of the Institute, which shall be final.

**Place:**

**Date:**

**Signature of Applicant**