

## SPONSORSHIP

(For applicants from AICTE approved institutions)

Prof./Dr./Mr./Mrs./Ms. \_\_\_\_\_  
is an employee of our institute and his/her application is hereby sponsored. The applicant will be permitted to attend the short-term course “**ADVANCES IN VLSI TECHNOLOGY AND SIGNAL PROCESSING**” at VSSUT, Burla to be held from 03 – 08 April, 2017, if selected.

Date: \_\_\_\_\_ Signature of  
sponsoring authority

Official Seal: \_\_\_\_\_ Designation

The duly sponsored application form should be mailed to:

**Dr. Sanjay Agrawal**  
**Dr. Kabiraj Sethi**

DEPARTMENT OF  
ELECTRONICS & TELECOMMUNICATION  
ENGINEERING.

VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY  
ODISHA, BURLA  
Sambalpur-768018, India

Tel. 919861032190, 919861143782  
Fax: - 0663-2430204

Email: agrawals.72@gmail.com  
kabirajsethi@yahoo.com

## ELIGIBILITY

The course is open to all teachers of degree level Technical/Engineering Colleges/Institutions approved by All India Council of Technical Education (AICTE) and having degree in related area of engineering. **No course fee** is charged for participants sponsored by AICTE approved institutions.

However, a caution money of **₹1000/-** in the form of demand draft drawn on any Nationalized Bank and in favor of ‘**Coordinator QIP STC ON AVLSITSP**’, payable at Burla, has to be sent by all the provisionally selected participants. It will be returned only when the participant joins the course.

## FINANCIAL ASSISTANCE

Limited number of participants from the AICTE approved institutions only will be eligible for sleeper class (second class) or III AC to and fro railway fare\* (via shortest route from the place of work).

## BOARDING & LODGING

Boarding and lodging facilities will be provided for the participants in Burla.

## IMPORTANT DATES

The last date for the receipt of duly forwarded applications is **4<sup>th</sup> March 2017**.

Intimation of provisional selection of candidature will be communicated through e-mail only by **11<sup>th</sup> March, 2017**.

Final selection notification will be given after receipt of DD and willingness to attend the course.

**PS:** Interested candidates may send an advance copy of the completed application form by fax/email to avoid procedural / postal delay.

## QIP SPONSORED SHORT TERM COURSE

ON

## ADVANCES IN VLSI TECHNOLOGY AND SIGNAL PROCESSING (AVLSITSP)

**03-04-2017 TO 08-04-2017**



### Coordinators

**Dr. Sanjay Agrawal**  
**Dr. Kabiraj Sethi**

### *Organized by*



**DEPARTMENT OF ELECTRONICS &  
TELECOMMUNICATION ENGINEERING**

**VEER SURENDRA SAI  
UNIVERSITY OF TECHNOLOGY, BURLA  
ODISHA-768018**

## SCOPE & OBJECTIVES

- To carry out quality research work.
- To focus on future challenges of VLSI and Signal Processing.
- Hands-on-experience with simulation techniques in the field of VLSI and Signal processing.
- To be conversant with technologies helpful for higher education.
- To be aware of challenges in the field of VLSI and Signal Processing.

## COURSE CONTENT

- High Level VLSI Design
- Low Power VLSI Design
- VLSI Technology
- VLSI Embedded System
- CAD VLSI
- Processor Architectures
- Image Processing
- Evolutionary Computing
- Signal Processing
- VLSI Signal Processing
- Computer Vision
- Smart Materials

## SPEAKERS

The course lectures will be delivered by the faculty members of VSSUT, Burla and few guest faculty invited from premier institutions of India.

## COURSE MATERIALS

Each registered participant will be provided with a set of comprehensive lecture notes.

## ABOUT US

Veer Surendra Sai University of Technology (VSSUT) Burla, Odisha was formed by Odisha Act 9 of 2009 by upgrading University College of Engineering (UCE), Burla to a non-affiliating Unitary State University, which came into force from 1<sup>st</sup> day of July 2009. The University has eleven departments covering all the major engineering, science and humanities disciplines, offering B.Tech., B.Arch., M.Tech., M.Sc. and Ph.D. programs. VSSUT is located at the foothill of Hirakud Dam – World's longest earthen dam. Burla is known as one of the intellectual capitals of Odisha with VSSUT, Sambalpur University, VSS Institute of Medical Science and Research, Chipilima Agriculture College and Indian Institute of Management (IIM) – all within a radius of 15 KMs. VSSUT is surrounded by metal and power industries and is referred to as Odisha's "Growth Corridor". VSSUT campus is about 10 kms from Sambalpur railway station. Weather in Burla in April will be bit warm but enjoying.

Department of Electronics and Telecommunication Engineering is the first department of its kind in the state of Odisha and was established in 1972 with a vision to develop new ideas in the field of communication. Since, more than four decades, this department has produced high quality and self-motivated professionals to serve humanity both nationally and internationally at par excellence.

## QIP Sponsored Short Term Course

on

### ADVANCES IN VLSI TECHNOLOGY AND SIGNAL PROCESSING (AVLSITSP)

03 – 08 APRIL, 2017

## Application Form

1. Name (block letters):
2. Designation & Pay-scale:
3. Organization:
4. Date of Birth:
5. Address for communication (with PIN code):

Phone:

Fax:

E-mail:

6. Academic Qualification (Please tick)

(a) B.Tech.                      (b) M.Tech.                      (c) Ph.D.

7. Specialization:

8. Experience (in years)

(a) Teaching                      (b) Research

9. Amount of TA required as per the entitlement mentioned in this brochure (only for AICTE approved college teachers) in ₹:

Please register me for the course on "*ADVANCES IN VLSI TECHNOLOGY AND SIGNAL PROCESSING*" to be held at VSSUT, Burla during 03–08 April, 2017.

Place:

Date:

Signature of the applicant