



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY : BURLA

(Formerly University College of Engg., Burla- Established by Govt. of Odisha in 1956 & Upgraded in 2009 to A State Govt. University Covered under Section 2(f) & 12(B) of UGC Act.)

P.O: Engineering College, Burla (Siddhi Vihar), Dist: Sambalpur
Odisha - 768018, INDIA

No. VSSUT/ 888 /

2017

Dated: 18-10-2017

NOTICE

The **Workshop on High Performance Computing** will be organized by **C-DAC, Pune** from **25-28 October 2017** at **Sir Visveswaraya Auditorium, VSSUT Burla**. The detailed programme is attached herewith for information of all concerned. All the interested faculty members and students are required to register by submitting a registration form in prescribed format to **PIC, Supercomputing Facility, Prof. A.K. Rath**, Professor of Computer Science and Engineering through their respective HODs **on or before 23rd October 2017 (Monday)**.

N.B: The number of seats for the workshop is limited to 150 only.

By order of Vice-Chancellor


REGISTRAR

Memo No. VSSUT/OOR/ 889 (30)

Dated: 18-10-2017

Copy to :

1. Prof. A.K.Rath, Professor of CSE & PIC, Supercomputing Facility for information and necessary action
2. All HODs for information and necessary action.
3. All Deans/COF for information necessary action.
4. Dean, Faculty & Planning with a request to hoist this notice in University website.
5. PIC, Electrical Maintenance/Civil Maintenance/CIF/CCF for information and necessary action.
6. ME/Campus Supervisor for information and necessary action
7. PA to VC for kind information of Hon'ble Vice-Chancellor


REGISTRAR

WORKSHOP ON HIGH PERFORMANCE COMPUTING

Organized by

Centre for Development of Advanced Computing (C-DAC)

Ministry of Electronics and IT, Government of India

At

Veer Surendra Sai University of Technology, Burla

Government of Odisha

Dates: October 25 – 28, 2017

Venue: VSSUT, Burla

Day 1 (Wednesday): October 25, 2017

Time (Hrs)	Sessions
10:00 – 11:00	High Performance Computing, IoT and Big Data <i>Keynote Speaker: Prof. Lalit Mohan Patnaik, IISc, Bangalore</i>
Break	
11:15 – 12:15	High Performance Computing, IoT and Big Data contd.. <i>Keynote Speaker: Prof. Lalit Mohan Patnaik, IISc, Bangalore</i>
12:15 – 13:00	Insight to Parallel Computing <i>Speaker: Shweta Das, C-DAC, Pune</i>
Lunch	
14:00 – 15:30	Intel architecture, software tools and technologies <i>Speaker: Jyotsna Khemka, Intel Corporation, Bangalore</i>
Break	
15:45 – 17:00	Intel architecture, software tools and technologies contd.. <i>Speaker: Jyotsna Khemka, Intel Corporation, Bangalore</i>

Day 2 (Thursday): October 26, 2017

Time (Hrs)	Sessions
10:00 – 11:00	Advance Parallel Computer Architecture <i>Speaker: Narendra Singh, C-DAC, Pune</i>
Break	
11:15 – 11:45	Insight to PARAM Shavak HPC and Deep Learning System Architecture and Systems <i>Speaker: Shweta Das, C-DAC, Pune</i>
11:45 – 12:15	Programming Aspects with Linux OS <i>Speaker: Pallav Jain, C-DAC, Pune</i>
12:15 – 13:00	Open Multi-Processing (OpenMP) <i>Speaker: Shweta Das, C-DAC, Pune</i>
Lunch	
14:00 – 15:30	Open Multi-Processing (OpenMP) Cont... <i>Speaker: Shweta Das, C-DAC, Pune</i>
Break	
15:45 – 16:30	Demonstration of Applications using OpenMP <i>Speaker: Shweta Das, C-DAC, Pune</i>
16:30 – 17:00	Compute Unified Device Architecture (CUDA) Overview <i>Speaker: Samrit Maity, C-DAC, Pune</i>

