



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA
(Formerly University College of Engineering, Burla-Established by Govt. of Odisha in 1956 & Upgraded in 2009
to A State Govt. University covered under Section 2(f) & 12(B) of UGC Act)
P.O: Engineering College, Burla(Siddhi Vihar), Dist: Sambalpur, Odisha – 768 018, India

No. VSSUT/PGSR/ 01 /19

Dated: 08 /02/19

NOTIFICATION

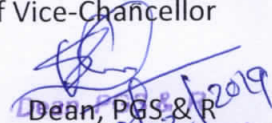
THE DEGREE OF DOCTOR OF PHILOSOPHY IN ENGINEERING

Mr. Debani Prasad Mishra, Regd. No. 15060010 is declared to be qualified for the award of the Ph.D Degree in Engineering (Electrical Engineering) with effective from 07.02.2019. The title of the thesis is “**AN INTELLIGENT APPROACH TO LOCATE AND CLASSIFY FAULT IN TRANSMISSION AND DISTRIBUTION LINE USING COMPUTATIONAL INTELLIGENT TECHNIQUE**”.

YEAR OF EXAMINATION – 2019

REGISTRATION NO. 15060010

By order of Vice-Chancellor


Dean, PGS & R
VSS University of Technology
Burla, Odisha, India-768018

Memo No.VSSUT/PGS&R/ 02(20)/19

Dated: 08.02.2019

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1. Mr Debani Prasad Mishra, S/o Sarat Chandra Mishra, At- IIT, Bhubaneswar, Gothapatana, Po-Malipada, Bhubaneswar-751003.
2. Dr.Papiya Ray, Associate Professor of Electrical Engineering, VSSUT, Burla – Supervisor
3. Prof Majid Jamil, Department of Electrical Engineering Jamia Millia Islamia, New Delhi -110025 , External Examiner of Ph.D Open Viva-voce.
4. Prof. Young Hwan Lho, Professor, Department of Railroad and Electrical Engineering, Woosong University, Daejeon, Republic of Korea. (External Foreign).
5. All DRC Members.
6. HOD & Chairman, DRC (of Mr. Debani Prasad Mishra), Electrical Engineering for information. *
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20. PA to VC for kind information of the Hon'ble Vice-Chancellor.


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