



No. VSSUT/PGSR/ 15 /19

Dated: 09 /02/19

NOTIFICATION

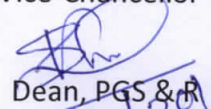
THE DEGREE OF DOCTOR OF PHILOSOPHY IN ENGINEERING

Mr. Priyadarsan Parida, Regd. No. 13060028 is declared to be qualified for the award of the Ph.D Degree in Engineering (Electronics & Telecommunication) with effective from 09.02.2019. The title of the thesis is **“DEVELOPMENT OF TRANSITION REGION BASED METHODS FOR IMAGE SEGMENTATION.”**

YEAR OF EXAMINATION – 2019

REGISTRATION NO. 13060028

By order of Vice-Chancellor



Dean, PGS & R

Memo No.VSSUT/PGS&R/ 16(20)/19

Dated: 09.02.2019

Copy forwarded to:

1. Mr. Priyadarsan Parida, S/o Brundaban Parida ,Plot No.391/944, Dulladei Rood, Kalyani Nagar, Cuttack-753013
2. Dr. Nilamani Bhoi , Associate Professor of Electronics and Telecommunication Engineering, VSSUT, Burla – Supervisor
3. Prof , Amitava Chatterjee, Professor, Electrical Engineering Department, Jadavpur University, Kolkata-700032
External Examiner of Ph.D Open Viva-voce.
4. Prof. Reyer zwigellaar, Department of Computer Science, Wbewystwyth University, wales, U.K. (External Foreign).
5. All DRC Members.
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20. PA to VC for kind information of the Hon'ble Vice-Chancellor.


09/2/2019
Dean, PGS & R

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