

VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

(A State Govt. University covered under Section 2(f) & 12(B) of UGC Act.)

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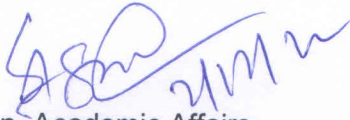


No.VSSUT/ACD/1099/2022

Dated: 02/12/2022

ADDENDUM NOTICE

In continuation to Notice No. VSSUT/ACD/1067/2022 Dated: 28.11.2022, the students of 5th semester only are hereby informed that an additional Open Elective subject i.e., Digital Circuit Design, offered by Department of EEE is included. The students can use the previously specified link to choose their Open Elective – II.


Dean, Academic Affairs
Dated: 02/12/2022

Memo No.VSSUT/ACD/200 /2022
Copy to:

1. All HODs / COE / PIC, Examination for information and necessary action.
2. PIC / Coordinator, University Time Table Committee for information.
3. Dean, Faculty & Planning for information with a request to upload this notice in the university website for wide circulation.
4. PA to VC for kind information of Hon'ble Vice Chancellor.


Dean, Academic Affairs