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Vice Chancellor, VSSUT, Burla

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Sri Tularam Kalet

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Organizing Committee :

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Vice - President Technical Society

Dr. Bidyadhar Rout

Branch Counsellor IEEE VSSUT SB

Prof. Harish Kumar Sahoo

Faculty Advisors IEEE VSSUT SB

Dr. Papia Ray

Dr. Suvendu Narayan Mishra

Dr. Ashish Kumar Sharma

Student Organizing Committee

Devi Prasad Pani, (Vice Chair - IEEE VSSUT SB,

Chair - IEEE Communication Society Chapter,

VSSUT SB)

Soumya Ranjan Maharana,

(Chair - IEEE VSSUT SB WIE AG)

Soumendra Priyadarshan,

(Chair - IEEE VSSUT SB)

Debabrata Sahoo, Student Member

Shirish Kumar Mishra, Student Member

About VSSUT

Veer Surendra Sai University of Technology (VSSUT), Odisha, established in 1956 in the name of University College of Engineering, Burla is the oldest engineering institution in the region. It was formed by Orissa Act 9 of 2009, transitioning from the University College of Engineering (UCE), Burla, to a non-affiliating unitary university. Recognised by the UGC, VSSUT offers undergraduate, postgraduate, and doctoral programmes in engineering, courses in architecture, and applied sciences. VSSUT offers multiple facilities in terms of central library, research laboratories, workshops, computing and internet services, hostels, sports, and health care. Through its exceptional faculty base and alumni network, the institution fosters innovation and development as a focus for academic and research excellence.

About ITR DRDO

The Integrated Test Range (ITR) of DRDO offers secure, reliable launch facilities for evaluating rockets, missiles, and air-borne systems. It features advanced instrumentation like EOTS, Radar, Telemetry, and Computer Systems for real-time tracking and health monitoring of flight vehicles. ITR's adherence to ISO 9001:2015 ensures quality and reliability. Continuous testing and simulations maintain readiness for diverse missions, enabling ITR to deliver critical data consistently.

About IEEE VSSUT SB

The IEEE VSSUT Student Branch, established at Veer Surendra Sai University of Technology, Burla, is a beacon of technical excellence and innovation. Inaugurated on February 14, 2019, it operates under the IEEE Kolkata Section and Bhubaneswar Sub-Section. With over 80 members, the branch fosters knowledge dissemination through seminars and workshops. Under the student branch, there are many Chapters and Affinity groups like Communication Society, Circuit and System Society and Women in Engineering Affinity Group designed to nurture and broaden the skills and interests of each member according to their specific areas of focus. The student society has been very successful at promoting scientific vision and pursuit among the students at VSSUT.



Two days Workshop

on

RGADT - 2024

**“ Recent Growth and
Advancement in Defence
Technology ”**

on

10 th & 11 th APRIL 2024

JOINTLY ORGANIZED BY



&



IEEE Com Soc Student Chapter VSSUT, Burla

&

IEEE Com Soc Chapter, ITR, DRDO, Chandipur

Technically Co-Sponsored by

IEEE Bhubaneswar Sub-section

IEEE Kolkata Section



Scope of Workshop

The "Recent Growth & Advancement in Defence Technology" workshop, will be presented by scientists of Integrated Test Range (ITR) DRDO, Chandipur in collaboration with the IEEE VSSUT Student Branch encompasses a comprehensive exploration of pivotal technologies shaping the defense sector. Expert sessions will address the incorporation of AI and ML in defense services, enhancing operational efficiency and decision-making processes. The workshop will also focus on RF communication within test ranges, discussing system range determination and performance factors. The advancement in dynamic RF target tracking will also be examined, highlighting the challenges in unstructured environments and the innovative solutions being developed. This collaborative event promises to offer a deep dive into the technological forefront of defense.

Target Participants

The Workshop is of immense interest to UG/PG students, research scholars/professionals, staff/faculty members, and industry professionals. The participants from different Science and Engineering backgrounds will benefit from this Workshop.

Point of Contact

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Resource Persons

Dr. Pradipta Roy, Scientist 'F'
ITR DRDO Chandipur

Mr. A K Das Scientist 'F'
ITR DRDO Chandipur

Dr. A K Ray, Scientist 'G'
ITR DRDO Chandipur

Important Dates

Registration Start	03 April 2024
Registration End	08 April 2024
Registration Confirmation	09 April 2024
Maximum number of participants (first come first serve basis)	150
Workshop Schedule	10th and 11th April 2024 (9 AM to 5 PM)

Address for Communication

Veer Surendra Sai University Of Technology
Burla , Sambalpur, Odisha
PIN-768018

Reach VSSUT

Nearest Railway Stations:

- Sambalpur Junction Railway Station (10 kms distance)
- Hirakud Station (2 kms distance)
- Jharsuguda station (55 kms distance)

Nearest Airport:

- Veer Surendra Sai Airport - Jharsuguda (60 kms distance)

Registration Details

Non-IEEE Student Members (UG, PG, PHD)	Rs. 180/-
IEEE Student Members	Rs. 150/-
Faculty from Academic Institutions	Rs. 250/-
Employees From Industry and R&D Organizations	Rs. 300/-

(Registration fees includes Registration Kits,
Refreshments, Tea, Snacks)

Bank Account Details For Registration

Account Name:	IEEE VSSUT BURLA STUDENT BRANCH
Account No.:	80622250004679
Bank Name:	Canara Bank
IFSC Code:	CNRB0018062

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