



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA
(Formerly University College of Engineering, Burla-Established by Govt. of Odisha in 1956 & Upgraded in 2009 to A State Govt. University covered under Section 2(f) & 12(B) of UGC Act)
P.O: Engineering College, Burla(Siddhi Vihar), Dist: Sambalpur, Odisha – 768 018, India

No. VSSUT/PGSR/ 17 /19

Dated: 11 /02/19

NOTIFICATION

THE DEGREE OF DOCTOR OF PHILOSOPHY IN ENGINEERING

Mr. Rosy Pradhan, Regd. No. 15060009 is declared to be qualified for the award of the Ph.D Degree in Engineering (Electrical Engineering) with effective from 11.02.2019. The title of the thesis is **“APPLICATION OF META-HEURISTICS OPTIMIZATION TECHNIQUES IN DESIGNING FRACTIONAL ORDER PID CONTROLLER.”**

YEAR OF EXAMINATION – 2019

REGISTRATION NO. 15060009

By order of Vice-Chancellor

Dean, PGS & R
VSS University of Technology
Burla, Odisha, India-768018

Memo No.VSSUT/PGS&R/ 18(20)/19

Dated: 11.02.2019

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3. Prof. G. Ravi, Professor & Head, Electrical & Electronics Engineering Department, Pondicherry Engineering College, Pondicherry-605014 (India) External Examiner of Ph.D Open Viva-voce.
4. Prof. Patrice WIRA, Professor of Computer Science, Member MIPS Laboratory, Faculty of Science & Technology, University of Haute Alsace, Mulhouse, France (External Foreign).
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