



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA
(Formerly University College of Engineering, Burla-Established by Govt. of Odisha in 1956 & Upgraded in 2009 to A State Govt. University covered under Section 2(f) & 12(B) of UGC Act)
P.O: Engineering College Burla (Siddhi Vihar), Dist: Sambalpur, Odisha – 768 018, India

No.VSSUT/PGSR/226 /19

Date: 17/04/2019

NOTIFICATION

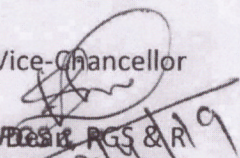
THE DEGREE OF DOCTOR OF PHILOSOPHY IN ENGINEERING

Mr. Shasanka Sekhar Rout Regn.No.15060013 is declared to be qualified for the award of the Ph.D Degree in Engineering (Electronics & TC Engineering) with effective from 17.04.2019. The title of the thesis is "DESIGN OF CMOS FREQUENCY CONVERSION CIRCUIT FOR RECEIVER FRONT END".

YEAR OF EXAMINATION – 2019

REGISTRATION NO. 15060013


By order of Vice-Chancellor


Dean, PGS & R
VSS University of Technology
Burla, Odisha, India-768018
Dated: 17.04.2019

Memo No. VSSUT/PGS&R/227 (20)/19

Copy forwarded to:

1. Mr. Shasanka Sekhar Rout S/o-Dibakar Rout, At-/Po- Putilinga, Via-Tyandakur, Dist- Kendrapara, Pin-754134, Odisha ,
2. Dr. Kabiraj Sethi, Associate Professor, Department of Electronics & TC Engineering, VSSUT, Burla – Supervisor
3. Prof. Satyabrata Jit, , Professor of Electronics & TC Engineering, Department of Electronics & TC Engineering, IIT (BHU) Varanasi-221005 (India) External Examiner, of Ph.D Open Viva-Voce.
4. Prof. Dr. Saraju P Mohanty, University of North Texas, Denton, TX, USA (External Foreign).
5. . All DRC Members.
6. HOD & Chairman, DRC (of Mr. Shasanka Sekhar Rout), Department of Electronics & TC Engineering, VSSUT, Burla for information.
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20. PA to VC for kind information of the Hon'ble Vice-Chancellor.


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