



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA
(Formerly University College of Engineering, Burla-Established by Govt. of Odisha in 1956 & Upgraded in 2009 to A State Govt. University covered under Section 2(f) & 12(B) of UGC Act)
P.O: Engineering College Burla (Siddhi Vihar), Dist: Sambalpur, Odisha – 768 018, India

No.VSSUT/PGSR/ /19

Date: 11/05/2019

NOTIFICATION

THE DEGREE OF DOCTOR OF PHILOSOPHY IN ENGINEERING

Mr. Deepak Kumar Patel, Regn.No.14060030 is declared qualified for the award of the Ph.D Degree in Engineering (Computer Science & Engineering) with effect from 11.05.2019. The title of the thesis is "ON LOAD BALANCING MECHANISMS WITH GRIDSIM".

YEAR OF EXAMINATION – 2019

REGISTRATION NO. 14060030

By order of Vice-Chancellor

Sd/-
Dean, PGS & R

Memo No. VSSUT/PGS&R/ 283 (22)/19

Dated: 11.05.2019

Copy forwarded to:

1. Mr. Deepak Kumar Patel, S/o- Bijaya Patel, At/Po- Plot No.N-5/8, I R C Village, Nayapalli, Bhubaneswar-751015
2. **Prof. C. R. Tripathy , Professor, Department of Computer Science & Engineering, VSSUT, Burla – Supervisor**
3. **Prof. Utpal Biswas, Professor, Department of Computer Science & Engineering, University of Kalyani, Nadia, West Bengal -741235 (India) External Examiner, of Ph.D Open Viva-Voce.**
4. Prof. Atakan Dogan, Professor, Eskisehir Technical University, -26470 Eskisehir, Turkey (External Foreign Examiner).
6. All DRC Members.
7. HOD & Chairman, DRC (of Mr. Deepak Kumar Patel), Department of Computer Science & Engineering, VSSUT, Burla for information.
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21. OS (Acad)/DA(PGSR)/Guard File(PGSR)/Personal file of Ph.D Scholar.
22. PA to VC for kind information of the Hon'ble Vice-Chancellor.

Sd/-
11.05.2019

Dean, PGS & R
VSS University of Technology
Burla, Odisha, India-768018