



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

NOTICE

No.VSSUT/PGSR/ 1256 /18

Date: 10 /10/18

In pursuance to provision 15.6 of Ph.D Regulation, Mr. Saroj Padhan, Regn. No. 12060004 of Electrical Engineering Department shall defend his Ph.D thesis before the Viva-Voce Board consisting of all DRC members and External Examiner on **30.10.2018 at 11.00 AM** in the Seminar Hall (Room No. D/220) of Electrical Engineering.

Thesis Title: "APPLICATION OF SOFT COMPUTING TECHNIQUES TO AUTOMATIC GENERATION CONTROL PROBLEMS".

All the faculty members and the students of the University are invited to attend the above "Open Viva-voce Examination".


Dean, PGS & R

10/10/2018

Memo No. VSSUT/PGS&R/1257/18

Dated: 10 /10/18

Copy to:

1. HOD, Electrical Engineering, for information and necessary action
2. Chairman DSC for information and necessary action
3. Dr. R. K. Sahu, Professor, EEE, Supervisor for information and necessary action.
The scholar may be advised to attend the "Open Viva-Voce Examination on appointed date and time complying with the instruction and make a presentation of his thesis, covering the **background, objectives, methodology, results and the conclusions** of his study.
4. Dr. S. Panda, Professor Electrical, Co-supervisor for information.
5. All the members of DRC for information and necessary action.
6. All the Heads of the Departments with a request for wide circulation among the faculty members and students for attending the above.
7. The Controller of Examination/Dean, Academic Affairs for kind information and necessary action with a request to attend the above.
8. Dean, Faculty & Planning for information with a request to hoist this notice in the University Website for information of all concerned.
9. P.A. to Vice-Chancellor, VSSUT for kind information of Vice-Chancellor.
Hon'ble Vice Chancellor is requested to attend the above "Open Viva-Voce Examination".


Dean, PGS & R

10/10/2018