



NOTICE

No. VSSUT/PGSR/ 174 /19

Date: 23.03.2019

In pursuance to provision 15.6 of Ph.D Regulation, Mr. Shasanka Sekhar Rout Regd. No. 15060013 of EL & TC Engineering Department shall defend his Ph.D thesis before the Viva-Voce Board consisting of all DRC members and External Examiner on 30.03.2019 at 11.00 AM in the ETC Seminar Hall of VSSUT, Burla.

Thesis Title: "DESIGN OF CMOS FREQUENCY CONVERSION CIRCUIT FOR RECEIVER FRONT END".

All the faculty members and the students of the University are invited to attend the above "Open Viva-voce Examination".


Dean, PGS & R
23/3/2019


Memo No. VSSUT/PGS&R/ 175(8) /19
Copy to:

Dated: 23.03.2019

1. HOD, **Electronics & TC Engineering**, VSSUT, Burla for information and necessary action
2. Chairman DSC for information and necessary action
3. Dr. Kabiraj Sethi, Associate Professor of Electronics & Tele communication Engineering, Supervisor for information and necessary action.

The scholar may be advised to attend the "Open Viva-Voce Examination" on appointed date and time complying with the instruction and make a presentation of his thesis, covering the **background, objectives, methodology, results and the conclusions** of his study.

4. All the members of DRC for information and necessary action.
5. All the Heads of the Departments with a request for wide circulation among the faculty members and students for attending the above.
6. The Controller of Examination/Dean, Academic Affairs for kind information and necessary action with a request to attend the above.
7. Dean, Faculty & Planning for information with a request to hoist this notice in the University Website for information of all concerned.
8. P.A. to Vice-Chancellor, VSSUT for kind information of Vice-Chancellor. Hon'ble Vice Chancellor is requested to attend the above "Open Viva-Voce Examination".


23/3/2019
Dean, PGS & R