



# VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

## NOTICE

No.VSSUT/PGSR/296/19

Date: 18.05.2019

In pursuance to provision 15.6 of Ph.D Regulation Mr. Bidyadhar Rout, Regd. No.11060005 of Electrical Engineering Department shall defend his Ph.D thesis before the Viva-Voce Board consisting of all DRC members and External Examiner on **11.06.2019 at 10.00 AM** in the Department of Electrical Engineering Hall No. D-220 of VSSUT, Burla.

**Thesis Title: "IMPROVEMENT OF POWER SYSTEM STABILITY USING COORDINATED DESIGN OF FACTS DEVICES AND EXCITATION CONTROLLERS".**

All the faculty members and the students of the University are invited to attend the above "Open Viva-voce Examination".

Dean, PGS & R

Memo No. VSSUT/PGS&R/297(8)/19


Date: 18.05.2019

**Copy to:**

1. HOD/ Chairman of DRC/ Department of Electrical Engineering for information and necessary action
2. Dr. B. B. Pati, Professor, Department of Electrical Engineering, VSSUT, Burla-Supervisor.

The scholar may be advised to attend the "Open Viva-Voce Examination" on appointed date and time complying with the instruction and make a presentation of his thesis, covering the **background, objectives, methodology, results and the conclusions** of his study.

3. All the members of DRC for information and necessary action.
4. All the Heads of the Departments with a request for wide circulation among the faculty members and students for attending the above.
5. The Controller of Examination/Dean, Academic Affairs for kind information and necessary action with a request to attend the above.
6. **Dean, Faculty & Planning for information with a request to hoist this notice in the University Website for information of all concerned.**
7. P.A. to Vice-Chancellor, VSSUT for kind information of Vice-Chancellor. Hon'ble Vice Chancellor is requested to attend the above "Open Viva-Voce Examination".

  
18/05/2019  
Dean, PGS & R  
VSS University of Technology  
Burla, Odisha, India-768018