



# VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY: BURLA

(A State Govt. University covered under Section 2(f) & 12(B) of UGC Act.)

P.O: Engineering College, Burla (Siddhi Vihar), Dist: Sambalpur  
Odisha- 768018, India, Website : [www.vssut.ac.in](http://www.vssut.ac.in)

No.VSSUT/ACD/ 16 /2021

Dated: 05/01/2022

## NOTICE

As per the resolution of the 27<sup>th</sup> Academic Council meeting held on 08/10/2021 and subsequent approval in the 40<sup>th</sup> meeting of Board of Management held on 06/11/2021, the backlog examinations for students having backlog subjects in Odd and Even Semesters will only be conducted along with supplementary examinations once in a academic year after the publication of Even Semester results.

However, the students currently having backlog in Even Semester are allowed to appear their backlog subjects in the coming Even Semester examinations of Academic Session 2021-22 as a special case due to the prevailing pandemic situation. No backlog examinations will be conducted along with the regular examinations further.

Memo No. VSSUT/ACD/ 12 /2022  
Copy to:

Dean, Academic Affairs  
Dated: 05/01/2022

1. All Deans/HODs/All PICs/Controller of Examination for information & necessary action.
2. Dean, Faculty & Planning with a request to facilitate in uploading of this notice in the University Website.
3. Registrar for information.
4. PA to VC for kind information of Hon'ble Vice Chancellor.

Dean, Academic Affairs