

VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY: BURLA

(A State Govt. University covered under Section 2(f) & 12(B) of UGC Act.)

P.O: Engineering College, Burla (Siddhi Vihar), Dist: Sambalpur
Odisha- 768018, India, Website : www.vssut.ac.in




No.VSSUT/ACD/725 /2023

Dated: 28/08/2023

CORRIGENDUM

In partial modification to the vide notice no. VSSUT/ACD/682/2023, Dated. 24/08/2023, the following students have taken admission under Lateral Entry Scheme in 3rd semester B.Tech for the Academic Session 2023-24 is allotted with the following correct registration number. The teachers concerned are requested to incorporate their names and registration numbers as given below in the respective Attendance-cum-Roll Sheet.

Sl.No.	Incorrect Regd. No.	Correct Regd. No.	Student Name	Branch Name	Section
1	2303050008	2303050007	Pichika Yugandhar	Electrical Engg.	B-1
2	2303050009	2303050008	Papun Patra	Electrical Engg.	B-1
3	2303050010	2303050009	Trilokanath Rout	Electrical Engg.	B-2
4	2303050011	2303050010	Varish Kumar	Electrical Engg.	B-2
5	2303050012	2303050011	Abhishek Biswal	Electrical Engg.	B-2


Dean, Academic Affairs
Dated: 28/08/2023

Memo No. VSSUT/ACD/726/2023

Copy to:

1. University Notice Board.
2. All HODs / Dean, Students' Welfare / COE/ COF / Prof. I/c Central Library / Examination Section / Account Section / Central Computer / Central Internet Facility / Workshop / Prof. T & P for information and necessary action.
3. Dean, Faculty and Planning with a request to facilitate in hoisting this notice in the university website.
4. PA to Registrar for information.
5. P.A. to Vice-Chancellor for kind information of Hon'ble Vice-Chancellor.

Sd/-
Dean, Academic Affairs