VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA, ODISHA, INDIA ବୀର ସୁରେନ୍ଦ୍ର ସାଏ ବୈଷୟିକ ବିଶ୍ୱବିଦ୍ୟାଳୟ



(A UGC Recognized State Government University by an Act of Assembly, Estd. -1956)
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Notification No. VSSUT/ PGS&R/

750

/ 2025

Dated:

29.5.25

PROVISIONAL

AWARD NOTIFICATION THE DEGREE OF DOCTOR OF PHILOSOPHY IN ENGINEERING

Ms. Bhawana Deshmukh, Registration No: 2010050101 is declared to have qualified for the award of the Ph.D. Degree in Engineering (Department of Electrical Engineering) with effect from 19.05.2025 (Monday). The title of the thesis is "ADVANCED FAULT DETECTION AND PROTECTION STRATEGIES FOR AC MICROGRID-INTEGRATED DISTRIBUTION NETWORKS".

YEAR OF EXAMINATION - 2025 REGISTRATION NO. - 2010050101

By order of Hon'ble Vice-Chancellor

Dean, PGS & R

VSS University of Technology Pare: Odifin India-768018

Memo.No.VSSUT/PGS&R/ 751 /2025

Copy forwarded to:

- 1. Ms. Bhawana Deshmukh, D/o- Lt. Shri Aniruddha Deshmukh, C/o-Vikash Deshmukh, Street No.- 1, Siddhivinayak Colony, Raigarh, 36/B, Dist.-Raigarh, Chhattisgarh, India.
- 2. Dr. Deepak Kumar Lal, Assistant Professor, Department of Electrical Engineering, VSSUT, Burla-768018 Supervisor
- 3. Dr. Sandeep Biswal, Associate Professor, Department of Electrical Engineering, OPJU. Raigarh- Co-Supervisor.
- 4. Prof. (Dr.) Salman Hameed, Professor, Department of Electrical Engineering, Z.H. College of Engineering and Technology, Aligarh Muslim University, Aligarh, India—External Examiner of Ph.D. Open Viva-voce (Indian Examiner).
- 5. Prof. Akhtar Kalam, College of Engineering and Science, Victoria University, Australia –External Examiner (Foreign).
- 6. The Chairman and all members of DRC of Ms. Bhawana Deshmukh, Department of Electrical Engineering, VSSUT, Burla for information.
- 7. The Director of Printing & Publication, Govt. Press, Madhupatna, Cuttack, Odisha, Pin-753010 for publication in the next issue of Odisha Gazette. <u>Free of Cost</u>.
- 8. The Principal Secretary to Chancellor, Raj Bhavan, Bhubaneswar, Pin-751008, for kind information.
- 9. The Principal Secretary to Government of Odisha, Skill Development & Technical Education Department, 2nd floor, Kharvela Bhawan, Bhubaneswar, Pin-751001, for kind information.
- 10. The University Grants Commission (UGC), Bahadur Shah Zafar Marg, New Delhi 110002.
- 11. The Secretary/ Head Libraian, University News, Association of Indian Universities, AIU House, 16, Comrade Indrajit Gupta Marg, Opposite National Bal Bhawan, Near ITO, New Delhi -110002, with a request to publish in University news. Free of Cost.
- 12. The Editor, The Samaj/ The Sambad/ The Dharitri/The Samaya/ The Prameya/ The New Indian Express/ The Times of India, with a request to publish the same in their News Daily, 110002. Free of Cost.
- 13. The Controller of Examinations/All Deans/All HODS/COF, VSSUT, BURLA for information.
- 14. Dean, Faculty & Planning, VSSUT, Burla for kind information with a request to hoist this notification in the University Website for information of all concerned.
- 15. PA to VC for kind information of Vice-Chancellor.
- 16. Guard File (PGSR)/Personal file of Ph.D. Scholar.

DEAN, PGS&R

Dean, PGS & R VSS University of Technology Burla, Odisha, India-768018