



No.VSSUT/SRIC/515/2026

VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY
PO- ENGINEERING COLLEGE BURLA, DIST-SAMBALPUR, ODISHA-
768018
www.vssut.ac.in

Date: 18/09/2026

WALK-IN (OFFLINE) INTERVIEW
JUNIOR RESEARCH FELLOW & PROJECT ASSOCIATE POSITIONS UNDER ANRF-PAIR
SCHEME

Title of Project: Consortium of Technologies for Sustainable Agriculture, Health, Energy And Environment
Date & Time of Interview: 9.10.2026, 9.00AM
Venue: Golden Jubilee Seminar Hall

The Veer Surendra Sai University of Technology, Burla invites candidates to attend a **Walk-in Interview** for the positions of **Junior Research Fellow (JRF)** and **Project Associate (PA)** under the ANRF-PAIR scheme. Interested candidates are advised to send a **single PDF file** containing the completed application form and supporting documents (Mark Sheets, Certificates, Research Publications, if any) via e-mail, mentioning the "Advertisement No." in the subject line, to the following email address: anrfpair@vssut.ac.in

Closing date of online application: 8.10.2026, 5.00 PM

Candidate are required to bring the hard copy of **duly filled and signed application form** (available on the University website: www.vssut.ac.in) along with **all supporting documents**, including mark sheets, certificates, and research publications (if any) at the time of the Walk-in Interview. No TA/DA will be paid for attending the interview.

Positions	Department wise No of Positions	Duration	Monthly Fellowship/Stipend
JUNIOR RESEARCH FELLOW (JRF) / PROJECT ASSOCIATE (PA)	Electrical & Electronics Engg - 01 Civil Engineering - 01	Coterminous with the project	JRF: First 2 Yrs. – Rs. 37000 Beyond 2 Yrs – Rs. 42000 PA: Rs. 30000 pm till the completion of project No HRA will be provided
PROJECT ASSOCIATE (PA)	Mechanical Engg – 01, Computer Sc and Engg – 01	Coterminous with the project	Rs. 30000 pm till the completion of project No HRA will be provided

N.B. – All the JRF & PA those who will be selected have to enroll into Ph.D Programme at VSSUT.

Application form and detailed information regarding eligibility and educational qualification for JRF and PA, is available in University website www.vssut.ac.in. The authority reserves the right to accept/reject any or all the applications or the entire selection process without assigning any reason thereof.

REGISTRAR

Memo No. VSSUT/SRIC/516/2026

Copy to:

1. University Notice Board.
2. All Deans/HODs/COE for information & necessary action.
3. Director I & PR Department, Government of Odisha, Bhubaneswar, for the publication of PhD Admission Notice in all Odisha edition of, "The Samaj" (Odia) on 19/09/2026.
4. Dean, Faculty and Planning, with a request to display the advertisement in University website.
5. Registrar/Comptroller of Finance for information.
6. P.A to Vice-Chancellor for kind information of Hon'ble Vice-Chancellor.

Date: 18/09/2026

REGISTRAR

	Project Code	Post	Broad area of research	Essential Qualification	Department/ Location of Interview	Co-PI
Agriculture						
	A3.4	Project Associate (PA)	Smart Agri-Quality Grading System: AI-Driven Assessment and Grading of Agricultural Products for Market Readiness	Essential Qualification (any one): B.E./B.Tech in Electronics, Computer Science, EEE and its relevant Engineering discipline/ M.Sc Computer Science/ M.Sc. in Electronics / M.Tech in Electronics/M.Tech in Computer Science / MCA or its relevant subject/ Equivalent degree recognized by government institutions Desirable Skills (may vary by project): Knowledge of programming languages (Python/MATLAB/C/C++)/ Experience in data analysis, machine learning, AI/ML/ Experience in laboratory experiments or field data collection	CSE	Santi Kumari Behera b.santibehera@gmail.com
Energy						
	E2.6	Project Associate (PA)	Phase change materials and battery TMS	Essential Qualification: B.Tech in Mechanical Engineering with 1st class. Desirable qualification: M.Tech in Thermal Engineering with 1st class	ME	Prof Aurovindo Mohanty amohanty_me@vssut.ac.in

				Familiar with Experiments and CFD simulation		
Environment						
	N 2.2	Jounior Research Fellow (JRF)/ Project Associate (PA)	Design and development of smart sensors and Li-Fi enabled sensor networks for detecting gas leakages	JRF: Essential qualification: Post graduate Degree in Professional courses selected through a process described through any one of the following: a) Scholars who are selected through National Eligibility Tests - CSIR - UGE NET, including lectureship (Assistant Professorship) and GATE. b) The selection process through National examinations conducted by Central Government Departments and their Agencies and Institutions, such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER, etc. PA: IF above qualification is not there B.Tech./ M.Tech. in EEE/ EE/ ECE/ ETC/ IT/ CS/ Instrumentation Engineering branches with or without GATE qualified OR M.Sc. Electronics/ Physics with or without NET qualified Preference shall be given to:	EEE	Gyan Ranjan Biswal gyanbiswal@vssut.ac.in

				(I) Degree holders to M.Tech. in the discipline of Electronics and Communication Engineering/ Electronics and Instrumentation Engineering/ Instrumentation Engineering with specialization in Communication Engineering/ Communication System Engineering/ Instrumentation Engineering with at least 60% in aggregate or 6.5 CGPA on 10-point scale. (II) GATE qualification. (III) Candidates with similar/related project experience		
	N 3.4.2	Junior Research Fellow (JRF)/ Project Associate (PA)	Development of Sustainable Self-Compacting Geopolymer Concrete through use of industrial and agricultural waste.	JRF: Essential Qualification: B.Tech/B.E. in Civil Engineering with a minimum CGPA of 6.5 (60%)/1st class with valid GATE/NET score, and M.E./M.Tech in Structural Engineering/Construction Technology and Management/ Construction Management/ Construction Technology with a minimum CGPA of 6.5/Percentage 60% /1st class from a recognized Technological University. The candidate must be selected through a process described through any one of the following: a) Scholars who are selected through National Eligibility	CIVIL ENGG	S. K. Panigrahi skpanigrahi_ce@vssut.ac.in

				Tests - CSIR-UGC NET, including lectureship (Assistant Professorship) and GATE. b) The selection process through National examinations conducted by Central Government Departments and their Agencies and Institutions, such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER, etc. PA: IF above qualification is not there		
--	--	--	--	---	--	--

CSE – 01

EEE – 01 (JRF/PA)

ME – 01

CIVIL E – 01 (JRF/PA)



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA, ODISHA, INDIA
APPLICATION FORM FOR WALK-IN INTERVIEW
JUNIOR RESEARCH FELLOW & PROJECT ASSOCIATE POSITIONS UNDER ANRF-PAIR SCHEME

Paste here
your
recent
passport
size

1. Advertisement No. : _____
2. Project Code : _____
3. Post Applied For : _____
4. Department to which Applying for : _____
5. Name of the candidate in full : _____
(Block Capital letters)
6. Father's Name : _____
7. Broad area only : _____
8. Name of Co-PI : _____
9. Address for Communication : _____

Telephone: _____ Mob: _____ Email: _____

10. Date of Birth: _____ 8. Sex : Male/Female 9. Marital status: Married /Single
10. Whether GEN/SC/ST _____ 11. Nationality: _____ 12. Mother Tongue: _____
13. Academic career: (Enclose attested copies of the certificates and mark sheets/grade cards showing the percentage of marks (CGPA) from H.S.C Examinations or equivalent)

Name of Exams	Institute/University	Year of passing	Branch/Subjects studied	Percentage of Marks/CGPA

Attach Supporting Documents

14. Whether Qualified Gate/Net (Yes/No) _____,
If yes mention Gate/Net Subject: _____, Qualifying Year : _____, Score: _____

15. Experience if any

Organization	Position	Duration	Nature of job

16. Research Publications, if any: (Enclose copies of published or unpublished work, Enclose extra sheet if required)

I do hereby declare that the information furnished in this application is true to the best of my knowledge and belief. If selected, I shall abide by rules and regulations of the University and Hall allotted to me. If any information furnished in this application is found to be untrue, I am liable to forfeit the JRF/PA positions to me any time in future and legal action be taken against me.

Date.....

Full signature of the Applicant

Enclosures: