

VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

OFFICE OF THE DEAN, SRIC&CEP

AND

COORDINATOR, QIP CENTRE

NOTICE

On scrutiny of the application received for Advance Admission in to Ph.D. Degree Programme-2015 under QIP Scheme, the following candidates are found fulfilling the norms to be call for interview by the respective department as mentioned below. The call letter for interview has been sent to the eligible candidates and they are required to report for interview as per the schedule in their respective department.

Department : Electrical Engineering (Code – VBEE01)

1. SASMITA PADHY
2. SATYAPRAKASH
3. PREETI RANJAN SAHU

Department : Electronics & Tele-Comm. Engineering (Code – VBEC01)

1. BIKASH MEHER
2. VYSYARAJU LOKESHRAJU

The following two candidates are not recommended for interview due to the reasons mentioned against their names.

Department : Electrical Engineering (Code – VBEE01)

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| 1. SANAT KUMAR PATRO | - B.Tech C.G.P.A. below 6.75 |
| 2. RUCHIR MOHAN SAMAL | - U.G. (AMIE) marks below 60% |

COORDINATOR, QIP