

SPONSORSHIP

For applicants from AICTE approved institutions.

Prof./Dr./Mr./Ms. _____ is an employee of our institute and his/her application is hereby sponsored. The applicant will be permitted to attend the short-term course “MODERN TRENDS & ADVANCES IN COMMUNICATION SYSTEMS & VLSI” at VSSUT, Burla to be held from 04 -09th January, 2016, if selected.

Date: _____ Signature of sponsoring authority

Official Seal: _____ Designation

The duly completed application should be mailed to:

Prof. Rutuparna Panda
Mr. Suvendu Narayan Mishra
QIP Short Term Course
**MODERN TRENDS & ADVANCES IN
COMMUNICATION SYSTEMS & VLSI(MTACSV)**

Department of ELECTRONICS &
TELECOMMUNICATION ENGG.
Veer Surendra Sai University of Technology Odisha, Burla
Sambalpur-768018, India
Tel.9861527445
Fax:-0663-2430204
Email:susoveny@ieee.org

ELIGIBILITY

The course is open to all teachers of degree level technical/engineering colleges/institutions approved by AICTE. No course fee is charged for participants sponsored by AICTE approved institutions. However, a caution deposit of Rs.1000/- has to be sent by the provisionally selected participants, which will be returned after joining the course. The payment is to be made by DD drawn on any Nationalized Bank and in favor of Coordinator QIP STC ON MTACSV, payable at Burla.

FINANCIAL ASSISTANCE

Limited number of participants from the AICTE recognized institutions will be eligible for III AC to and fro railway fare (via shortest route from the place of work).

BOARDING & LODGING

Boarding and lodging facilities shall be provided by the University for the candidates from AICTE approved institutions.

IMPORTANT DATES

The last date for receipt of duly filled in applications is 5th December 2015. Intimation of provisional selection of candidature will be communicated through e-mail by 10th December, 2015. Final selection notification will be given after receipt of DD and willingness to attend the course.

Interested candidates may send an advance copy of the completed application by fax/email to avoid procedural /postal delay.

VEER SURENDRA SAI
UNIVERSITY OF TECHNOLOGY ODISHA,
BURLA

**QIP SPONSORED SHORT TERM COURSE
ON
MODERN TRENDS & ADVANCES IN
COMMUNICATION SYSTEMS &
VLSI(MTACSV)**

04-01-2016 TO 09-01-2016

Coordinators

**Prof. Rutuparna Panda
Mr. Suvendu Narayan Mishra**



Organized by



**DEPARTMENT OF ELECTRONICS &
TELECOMMUNICATION ENGINEERING**