SHORT TERM COURSE

on

Technical Report Writing and Presentation using LaTeX

(TRWP-2022)

08th April 2022 – 09th April 2022

COORDINATOR

Dr. R. K. Samal

Organised By

Department of Electrial Engineering

Veer Surendra Sai University of Technology

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About the STC

LaTeX is a high-quality typesetting system; it includes features designed for the production of technical and scientific documentation. LaTeX is the de facto standard for the communication and publication of scientific documents. LaTeX is available as free software.

LaTeX is widely used in academia for the communication and publication of scientific documents in many fields, including mathematics, computer science, and engineering. It also has a prominent role in the preparation and publication of books and articles that contain complex multilingual materials. LaTeX uses the TeX typesetting program for formatting its output, and is itself written in the TeX macro language.

The Short Term Training Program on Technical Report Writing and Presentation using LaTeX aims to provide useful skills on project report and thesis writing using this versatile software. The course aims to equip students and expose them to the fact that the possibilities are limitless using LaTeX. Additionally, essentials components of a technical reports are highlighted.

OBJECTIVES

The objectives of this short term course to train undergraduate, postgraduate students and research scholars to prepare research papers, project reports, thesis and presentation using TeX and LaTeX. At the end of the course the participants are expected to be conversant in preparing above documents using LaTeX.

Course Contents

- Familiarization of formats of Project report and Thesis
- Essentials of Technical Report Writing
- Document preparation using TeX and LaTeX
- Report writing
- Mathematical Typesetting.
- Numbering equations.
- Tables and Figures
- Using Beamer for presentation
- Downloading and Installaing LaTeX.

Chief Patron

Prof. B. Majhi
Vice Chancellor (I/C), VSSUT, BURLA

Patron

Prof. P.R.Das
Dean, CDCE, VSSUT, Burla

Convener

Dr. Papia Ray,
Associate Professor & Head
EE Dept, VSSUT BURLA

Coordinator

Dr. R. K. Samal,
Assistant Professor,
EE Dept, VSSUT BURLA

REGISTRATION

Number of participants are limited to 100

Registration Fees of Rs. 250 (two hundred fifty rupees only) needs to be deposited in the following account (NEFT/Bank Challan)

Account No. 110043626802

Name: HOD Electrical Engineering VSSUT Burla

IFSC Code: CNRB0018062

Name of the Bank: CANARA BANK, VSSUT Burla branch

Online registration should be done in the below mentioned link before 05th April, 2022.

- ❖ **Enrolment will be on First come First Serve basis**
- ❖ **E-Certificate will be issued to participants who will attend for all sessions.**

https://forms.gle/BNVJHiKy4ik2QZY5A

ABOUT THE UNIVERSITY

Veer Surendra Sai University of Technology(VSSUT), Odisha was established in the year 1956 as the first engineering college of the State at the foot hill of the world famous Hirakud dam. The university was formed by Orissa Act 9 of 2009 by converting University College of Engineering (UCE), Burla to a non-affiliating Unitary University and came into force by issue of notification by the Industries Department, Government of Odisha from 1st day of July 2009. It has carved a niche for itself among the best technical institutions in India and is a dream institute for many budding engineers. The University offers a number of academic programmes namely, Bachelor of Technology (B.Tech), Bachelor of Architecture (B.Arch), Master of Technology (M.Tech), Master of Science(M.Sc.), Dual Degree, Master of Philosophy (M.Phil), Master in Computer Application(MCA) and Doctor of Philosophy(Ph.D.). The University is surrounded by a large number of Government, public and private industrial sectors such as JSW, OHPC, HINDALCO, NALCO, NTPC, OPTCL, MCL, Vedanta Aluminium Ltd and TATA BSL to name a few. The institute has an excellent placement record with a number of top ranking companies visiting the campus every year.

ABOUT THE DEPARTMENT OF ELECTRICAL ENGINEERING

The Department of Electrical Engineering, Veer Surendra Sai University of Technology (Formerly University College of Engineering: Burla, Estd. 1956), was established since inception of the institute and at present offers the four year undergraduate B.Tech, five year B.Tech and M.Tech. dual degree, two years M.Tech. and Doctoral Degree programs. It has produced high quality technocrats for the last few decades to cater to the needs of govt. Sector, PSU, industry, R&D organizations, and academic Institutions. The B.Tech. and M.Tech. (PSE) courses has been accredited by NBA and others are under consideration. The Department has highly qualified and experienced faculty members. The syllabi of the Department are updated with many advanced courses to enable students to keep themselves at par with the cutting edge technology. The teaching in the department emphasizes on fundamental principles, development of creative thinking and the analytical ability to solve real life problems. The Department also encourages its students to engage in extra-curricular and co-curricular activities and personality development.

ELIGIBILITY

The course is intended for research scholars, post-graduate students and final year undergraduate students. However, anyone interested to learn LaTeX can participate including faculty members and industrial executives.

RESOURCE PERSONS

The course will be conducted by faculty of Electrical Engineering and other departments of VSSUT Burla.

COURSE DURATION/VENUE

The course is of two days duration from 08th April 2022 – 09th April 2022. Classes will be held in the E-learning centre of VSSUT, Burla. The participants are encouraged to bring their personal laptops so that they can configure the software during the course.

FINANCIAL ASSISTANCE

No TA/DA will be paid to participants.