



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY: BURLA
(Formerly University College of Engg. Burla- Established by Govt. of Odisha in 1966 &
Upgraded in 2009 to A State Govt. University covered under Section 2(f) & 12(B) of UGC Act.)
P.O: Engineering College, Burla (Siddhi Vihar), Dist: Sambalpur
Odisha-768018, India, www.vssut.ac.in

No. VSSUT/OOR/777

Dated: 03/11/2023

WALK IN INTERVIEW FOR PROJECT FELLOW

The walk-in interview for the post of Project Fellow (One post) under the project sponsored by State Council on Science & Technology, Govt. Of Odisha under the principal investigator Prof. Ganeswar Nath, Professor, Department of Physics, Veer Surendra Sai University of Technology, Burla, Sambalpur, Odisha will be held on **11.11.2023 at time 10.00AM** in the Department of Physics. The candidates possessing M.Sc. in Physics from a recognized institute/university with minimum 55% marks are invited to attend an interview on the above said date and time. Preference will be given for NET/GATE students. Recruited Project Fellow should register for Ph.D. at this institute through proper channel as per VSSUT norms.

Preferable: Applicants have vast knowledge about the handling research instruments, data analysis, synthesis of composites, microwave analysis. The above experience is more preferable to the applicant.

As per State Council on Science & Technology, Govt. of Odisha the **fellowship: Rs.-17000/- per months for 1st year and Rs.19000/- for 2nd year.**

Interested and eligible candidates are requested to come with the hard copy of completed application (Annexure -1) along with self-attested copies of supporting documents.

Sahar
08/11/2023

REGISTRAR
(i/c)

VSS University of Technology
Burla, Sambalpur, Odisha-768018

Memo No. VSSUT/OOR/ 778

Dated: 03/11/2023

Copy to:

1. University Notice Board
2. The Dean Faculty & Planning with a request to display the advertisement in University website immediately
3. COF for information and necessary action
4. PA to VC for kind information of the Vice Chancellor
5. PA to Registrar for kind information

Sahar
03/11/2023

REGISTRAR
(i/c)
VSS University of Technology
Burla, Sambalpur, Odisha-768018

ANNEXURE-1

Application form for the Post of Project Fellow (PF) for DST, Govt. of Odisha, sponsored Research Project

Personal Details:

1. Full Name (In capital):
2. Date of Birth (DD/MM/YY) :
3. Category (GEN/SC/ST/OBC)
4. Marital Status (Single/Married)
5. Gender (Male/Female)
6. Nationality
7. Address for Communication
8. Permanent Address
9. Mobile/Phone No.
10. Email:

Affix a recent passport size photo

Details of School/University/Institute Studied (From Matriculation Onward)

Degree	Discipline	University/Institute	Year	%Marks /CGPA	Division
Class X					
Higher Secondary					
B.Sc.					
M.Sc.					
Any other					

Qualifying Examination (GATE/CSIR-NET/ICAR-NET/UGC-NET) if any:

Professional Experiences (Teaching/Research/Industrial) if any:

Name of Organization	Designation	Nature of Work	From	To

M.Sc. Thesis Title:

Seminar/workshop/conference attended (use extra sheet if needed):

Name of Seminar/Conference etc	Paper presented	Organized by

Research Publication including book and book chapters (use extra sheet if needed)

Awards, patents, prizes etc (use extra sheet if needed):

Any other Relevant Information (use extra sheet if needed):

DECLARATION:

I hereby declare that I have carefully read the instructions and particulars supplied to me and that the entries made in this application form are correct to the best of my knowledge and belief. If selected for admission, I promise to abide by the rules and discipline of the Institute. I note that the decision of the Institute is final in regard to selection. The Institute shall have the right to expel me from the Institute at any time after my admission, provided it is satisfied that I was admitted on false particulars furnished by me or my antecedents prove that my continuance in the Institute is not desirable. I agree that I shall abide by the decision of the Institute, which shall be final.

Place:

Date:

Signature of Applicant