



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY BURLA  
ବୀର ସୁରେନ୍ଦ୍ର ସାଏ ଟେକ୍ନୋଲୋଜି ବିଶ୍ୱବିଦ୍ୟାଳୟ  
(A UGC Recognized State Government University by an Act of Assembly, Estd. -1956)  
P.O. Engineering College, Burla, Dist: Sambalpur, Odisha, (India) -768 018  
[www.vssut.ac.in](http://www.vssut.ac.in). e-mail: [vc@vssut.ac.in](mailto:vc@vssut.ac.in)

No. VSSUT/ / / 2024

Dated:

**ADVERTISEMENT FOR THE POSITION OF JUNIOR RESEARCH FELLOW**

Applications are invited for the position of the Junior Research Fellow (JRF) under the supervision of Dr. Santi Behera , Department of Electrical and Electronics Engineering and Dr. Prasanta Kumar Pradhan in the department of Mechanical Engineering Veer Surendra Sai University of Technology Burla, Sambalpur, Odisha funded by IITG-TIDF. For more details visit the website: <https://www.vssut.ac.in>.

REGISTRAR

Memo No. VSSUT/SRIC/146.4/2024<sup>(6)</sup>

13.07.2024

Copy to:

1. Director I&PR Department, Govt. Of Odisha, Bhubaneswar, with a request to publish the above advertisement in one issue of the Odisha daily edition (ALL ODISHA EDITION) of THE SAMAJ using minimum space at I&PR approved/lowest rates on or before 19.07.2024 The bills may be sent to the registrar, VSSUT, Burla-768018 in TRIPLICATE along with copies of the paper in which the publication is made, for necessary payment.
2. The University/Department Notice Board for publicity.
3. PA to Registrar for kind information.
4. Dean F&P, with a request to hoist the notice in the University web site. For wide publicity.
5. Dean SRIC, for kind information.
6. HOD, EEE. For kind information.

REGISTRAR

REGISTRAR  
V.S.S. University of Technology  
Burla Sambalpur Odisha-768018

## VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA



(Formerly University College of Engg., Buria-Established by Govt. of Odisha in 1956  
& Upgraded in 2009 to A State Govt. University Covered under Section 2(f) & 12(18) of UGC Act.)

P.O: Engineering College, Burla (Siddhi Vihar), Dist: Sambalpur  
Odisha-768018, INDIA

### Advertisement for Post of Junior Research Fellow (One position) for IITG-TIDF sponsored project

Applications are invited from eligible students for the position of JRF (one-position) for the **IIT Guwahati Technology Innovation And Development Foundation** funded project, under the principal investigator Dr. Santi Behera, Associate Professor, Department of Electrical and Electronics Engineering (EEE) and Co-Principal Investigator Dr. Prasanta Kumar Pradhan, Department of Mechanical Engineering, Veer Surendra Sai University of Technology, Burla, Sambalpur, Odisha. Interested and eligible candidates are requested to send hard copy of completed application (Annexure -I) along with self-attested copies of supporting documents to the Registrar, Veer Surendra Sai University of Technology, Burla, Sambalpur-768018, on or before 23<sup>rd</sup> July 2024 by speed/registered post/courier. A copy of the application should also be mailed to santi\_eee@vssut.ac.in. The details of the project are as follows:

Name of the Project	Sensor-based Health monitoring of large dam in hydro power plant with AI integral approach.
Principal Investigator & Co-Principal Investigator	Dr Santi Behera and Dr Prasanta Kumar Pradhan
Essential Qualification	<p>B. Tech in Mechanical Engineering/Electrical Engineering/EEE/Civil Engineering from a recognized institute/ university</p> <p><b>Preferable:</b></p> <ul style="list-style-type: none"><li>• Experience in theoretical studies related to structural dynamics, including vibration analysis and response spectrum methods.</li><li>• Applicants with a deep understanding of soft computing technique.</li><li>• Ability to conduct experimental studies or numerical simulations to investigate the behaviour of dam structures.</li><li>• Having GATE score.</li></ul>
Salary/Remuneration	As per IITG-TIDF Rules. For. Project fellowship: Rs.-25. 000 /-

	per month.
Nature and duration of post	The appointment will be purely on temporary basis and will be terminus with the project. The PI and Institute reserve the right to terminate the engagement of the JRF at any time if the performance is not found satisfactory. The person engaged shall not be entitled for any claim implicit or explicit for permanent absorption in the University.
Age Limit	30 years

**General Term and Condition:**

1. Interested and eligible candidates are requested to send Hard copy of completed application (Annexure -1) along with self-attested copies of supporting documents to the, **Registrar, Veer Surendra Sai University of Technology, Burla, sambalpur-768018**, on or before **23<sup>rd</sup> July, 2024** by speed/registered post/courier. University will not be responsible for any postal delay of application. **Applications received after the above mentioned date will be treated as cancelled. On the envelope, please inscribe "Application for the post of JRF (IITG-TIDF)", Department of Electrical and Electronics Engineering, VSSUT Burla.**
2. Candidates will be short listed for the interview based on the specialization and availability of candidates and other requirements as per the project.
3. The date and time of interview will be informed via email or phone and only short listed candidates will be allowed to attend the interview.
4. Candidates shortlisted for interview should appear in person with originals of certificates (one set of Xerox), and date of birth proof along with any other relevant information (like copies of publications, awards etc.)
5. No travelling or any other allowances is admissible for attending the interview.
6. Candidates will not be considered in absentia.
7. The Principal Investigator of the project reserves the right to fill or not fill position or withdraw the notification at any point of time and in any conflict the decision of PI and Institute will be final.
8. All legal implications are subjected to local jurisdiction only.

**Dr Santi Behera**  
(Principal Investigator)

**Dr Prasanta Kumar Pradhan**  
(Co-Principal Investigator)



### ANNEXURE-I

Application form for the Post of Junior Research Fellow(JRF) for IITG-TIDF sponsored project

#### **Personal Details:**

1. Full Name (In capital):
2. Date of Birth (DD/MM/YY) :
3. Category (GEN/SC/ST/OBC)
4. Marital Status (Single/Married)
5. Gender (Male/Female)
6. Nationality
7. Address for Communication
8. Permanent Address
9. Mobile/Phone No.
10. Email:

Affix a recent  
passport size  
photo

11. Details of School/University/Institute Studied (From Matriculation Onward)

Degree	Discipline	University/Institute	Year	%Marks /CGPA	Division
Class X					
Higher Secondary					
B.Tech/BE					
M.Tech					
Any other					

12. Qualifying Examination (GATE/CSIR-NET/ICAR-NET/UGC-NET) if any:

13. Professional Experiences (Teaching/Research/Industrial) if any:

Name of Organization	Designation	Nature of Work	From	To

14. Research Publication including book and book chapters (use extra sheet if needed)
15. Awards, patents, prizes etc. (use extra sheet if needed):
16. Any other Relevant Information (use extra sheet if needed):

17. DECLARATION:

I hereby declare that I have carefully read the instructions and particulars supplied to me and that the entries made in this application form are correct to the best of my knowledge and belief. If selected for admission, I promise to abide by the rules and discipline of the Institute. I note that the decision of the Institute is final in regard to selection. The Institute shall have the right to expel me from the Institute at any time after my admission, provided it is satisfied that I was admitted on false particulars furnished by me or my antecedents prove that my continuance in the Institute is not desirable. I agree that I shall abide by the decision of the Institute, which shall be final.

Place:

Date:

**Signature of Applicant**